

ENGINE



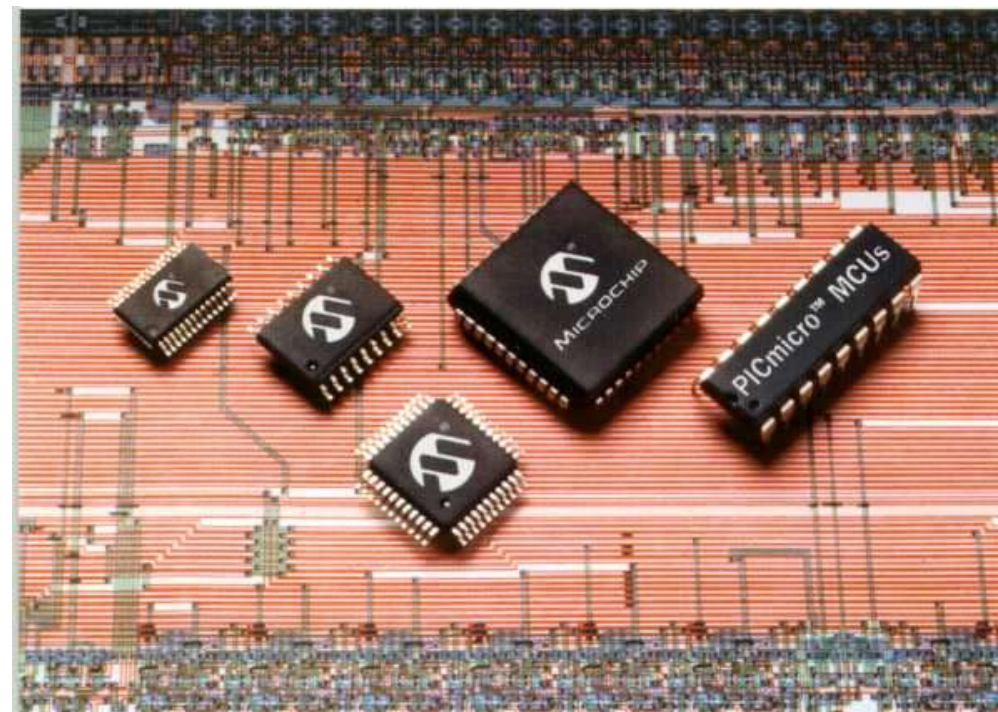
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TEACHING ONLINE ELECTRONICS, MICROCONTROLLERS AND PROGRAMMING
IN HIGHER EDUCATION

Module_2-1. Pins as outputs

Περιεχόμενα

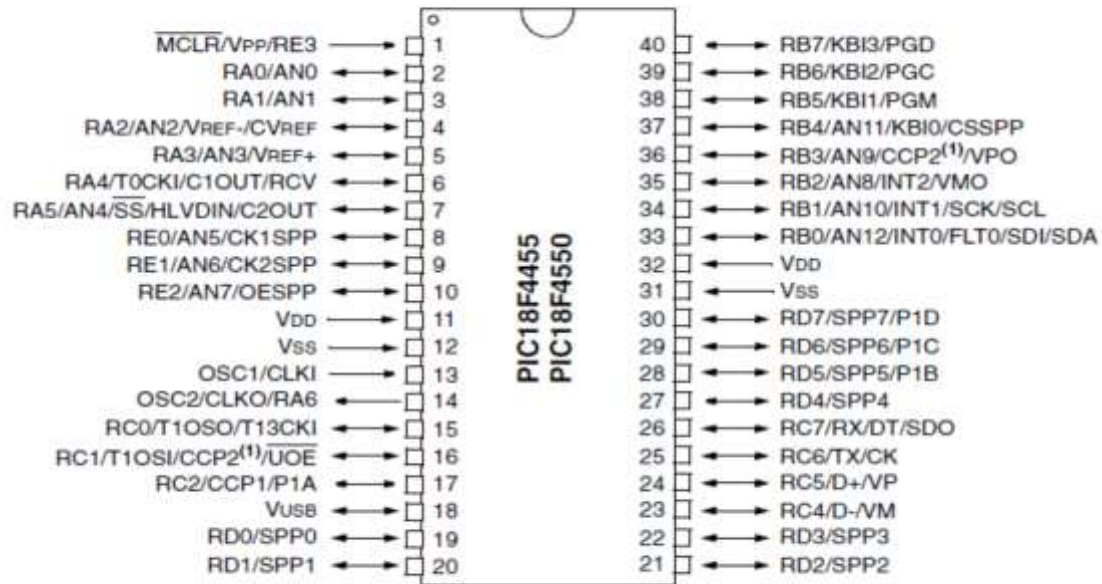
- Εισαγωγή
- Παράδειγμα



PIC18F4550

Pins as outputs *Εισαγωγή*

40-Pin PDIP



Pins as outputs

Εισαγωγή

Direction / Data register

PORTB Direction Register

PORTB Direction Register, TRISB, Special Function Register

Στην διεύθυνση F93h.

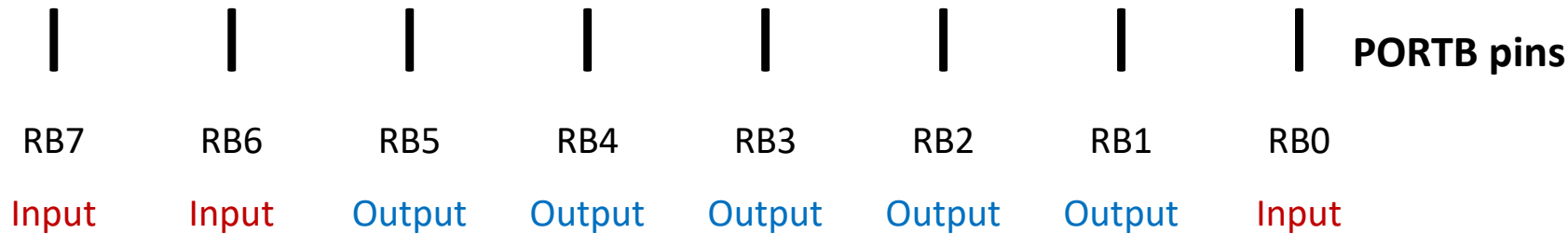
1	1	0	0	0	0	0	1
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PORTB Data Register

PORTB Data Register, PORTB, Special Function Register

Στην διεύθυνση F81h.

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Special Function Registers

F97h	TRISD ⁽³⁾
F96h	TRISE ⁽³⁾
F95h	TRISD ⁽³⁾
F94h	TRISC
F93h	TRISB
F92h	TRISA
F91h	— ⁽²⁾

F84h	PORTE
F83h	PORTD ⁽³⁾
F82h	PORTC
F81h	PORTB
F80h	PORTA

Οι τιμές στον TRISB καθορίζουν ποια pins της PORTB θα είναι είσοδοι και ποια έξοδοι.

- 1 → PORTB **input**.
- 0 → PORTB **output**.

set_tris_b(0xF0);

set_tris_b(0b11110000);

ON/OFF LEDs κάθε 200 ms

Pins as outputs *Παράδειγμα*

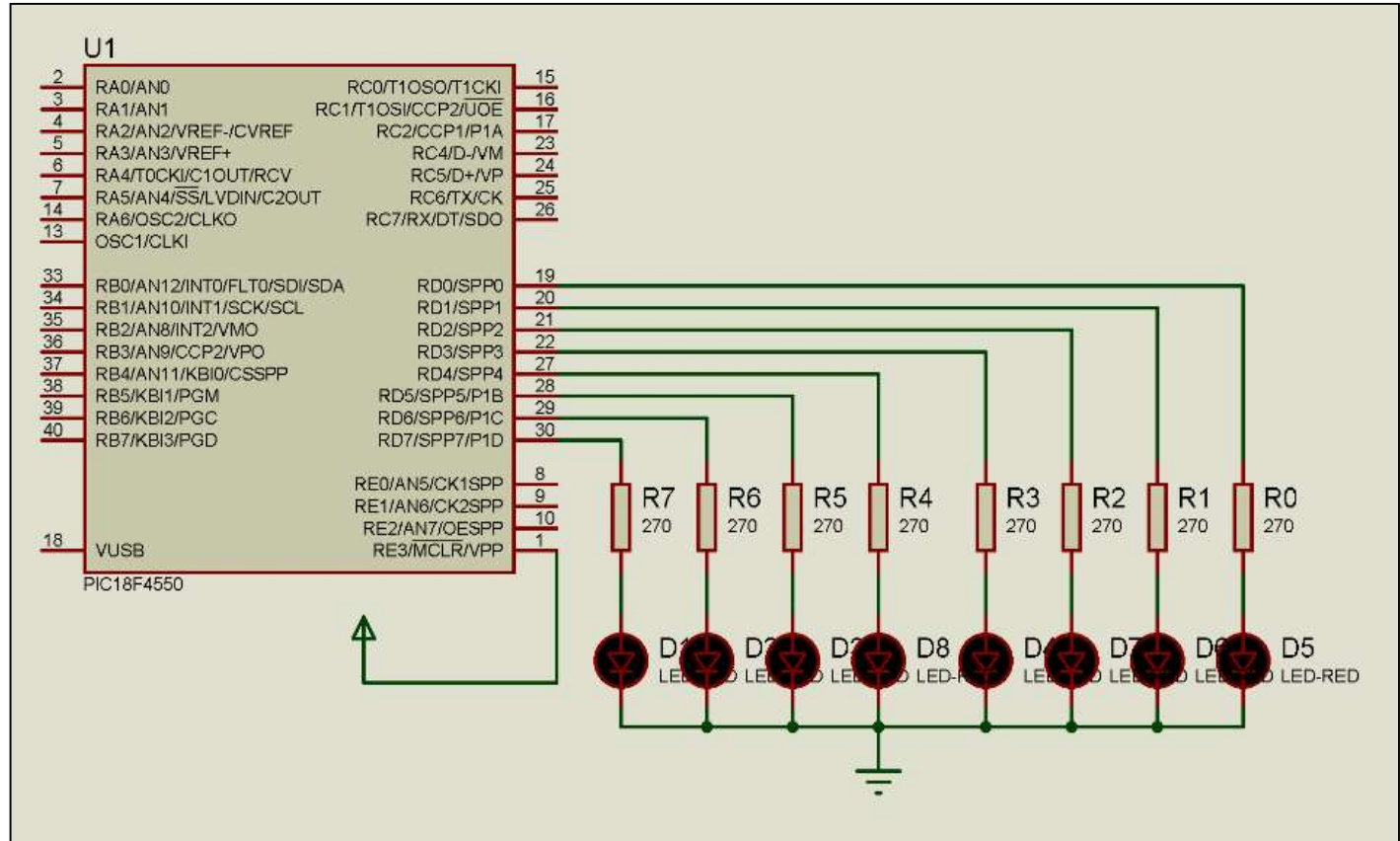
```
#include <main.h>
#byte PORTD = 0xF83 // F83h θέση μνήμης της PORTD

void main(void){

set_tris_d(0x00); // η PORTD ορίζεται ως έξοδος

PORTD = 0b11111111; //η PORTD παίρνει την τιμή 1111 1111

while(TRUE) {
    delay_ms(100); // για πάντα //περίμενε 100 ms
    PORTD=0b00000000; // σβήσε τα 8 LEDs
    delay_ms(100); //περίμενε 100 ms
    PORTD=0b11111111; //άναψε τα 8 LEDs
}
}
```



ENGINE Partnership

- Warsaw University of Technology (PL) - *coordinator*
- IHU - International Hellenic University (GR)
- EDUMOTIVA - European Lab for Educational Technology (GR)
- University of Padova (IT)
- University of Applied Sciences in Tarnow (PL)



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