



Erasmus+

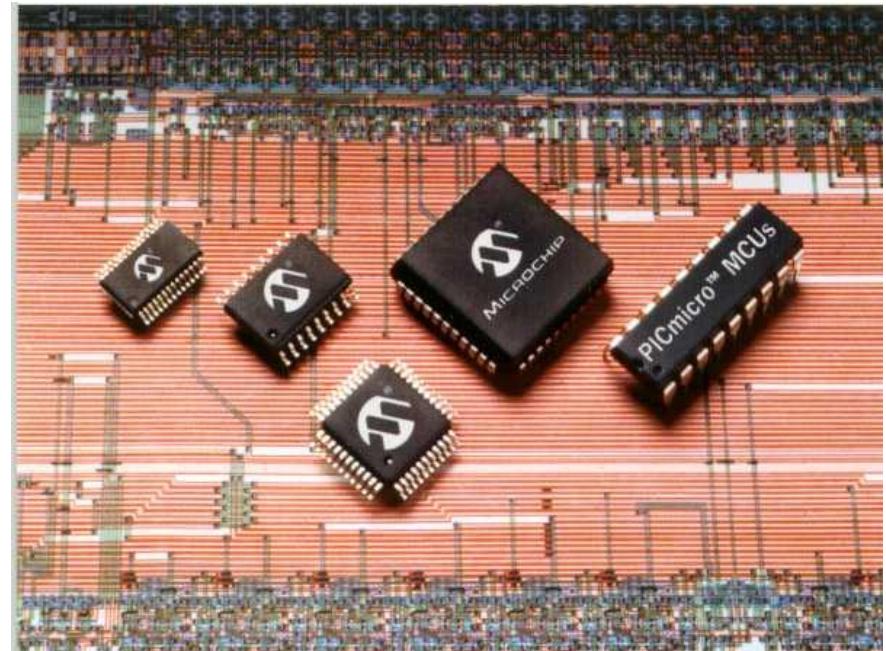
TEACHING ONLINE ELECTRONICS, MICROCONTROLLERS AND PROGRAMMING  
IN HIGHER EDUCATION

# Module\_2-8. Timers

*PIC18F4550 with Proteus Simulation*

# Contents

- Interrupt
- Timer



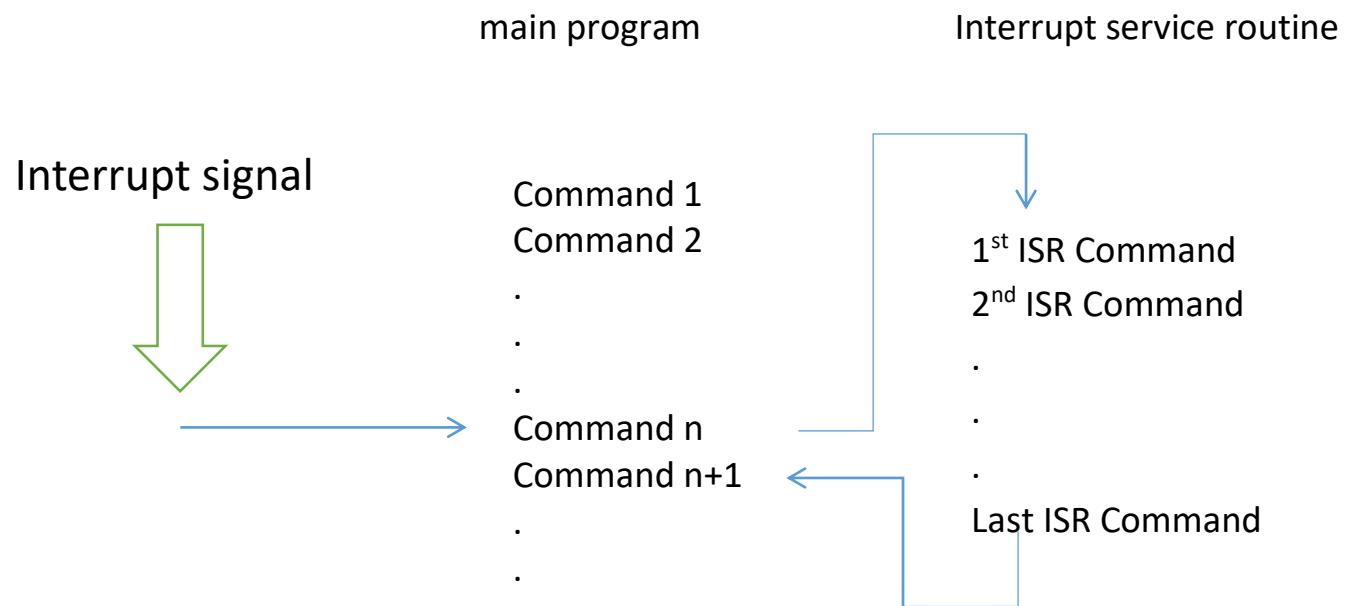
# *Module\_2-8. Timers*

## *Interrupt*

### Interrupts

Interrupt is to stop execution of the microcontroller's program flow in order to execute a block of code called an interrupt service routine

Example:



## *Module\_2-8. Timers Timer*

Timers are some 8-bit or 16-bit registers whose content is incremented by a specific time interval.

After the value 111 ... 111 the timer takes the value 000 ... 000 (overflow). In this transition we can program an interrupt to occur.

Question: Where do the pulses that increment the Timer value come from?

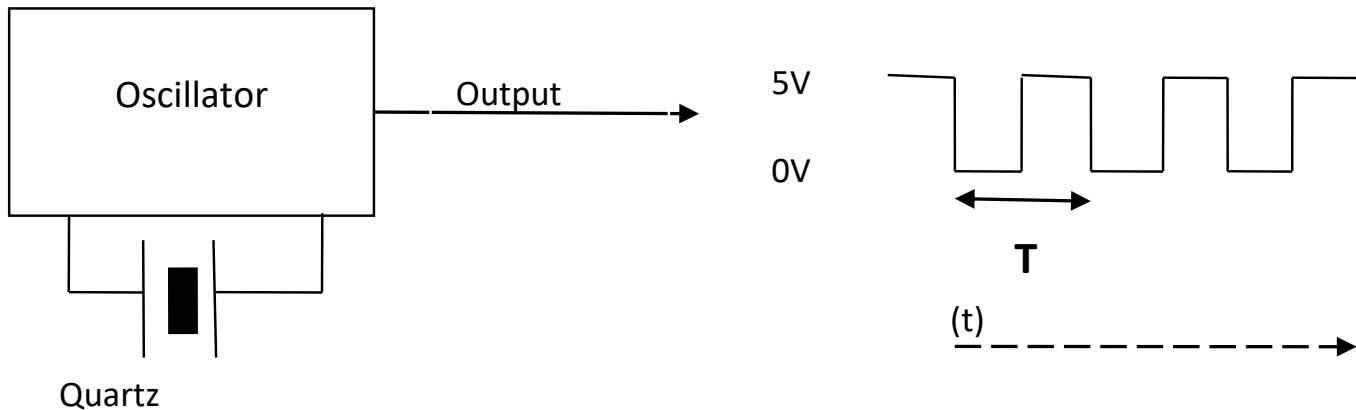
Answer: They can come from an external source and be applied to a specific pin of the microcontroller

Or come from a circuit inside the microcontroller called an internal oscillator (Oscillator)

In our program with a command we declare the source of the pulses that we want to be used

## *Module\_2-8. Timers*

### *Timer*



$$f = \frac{1}{T}$$

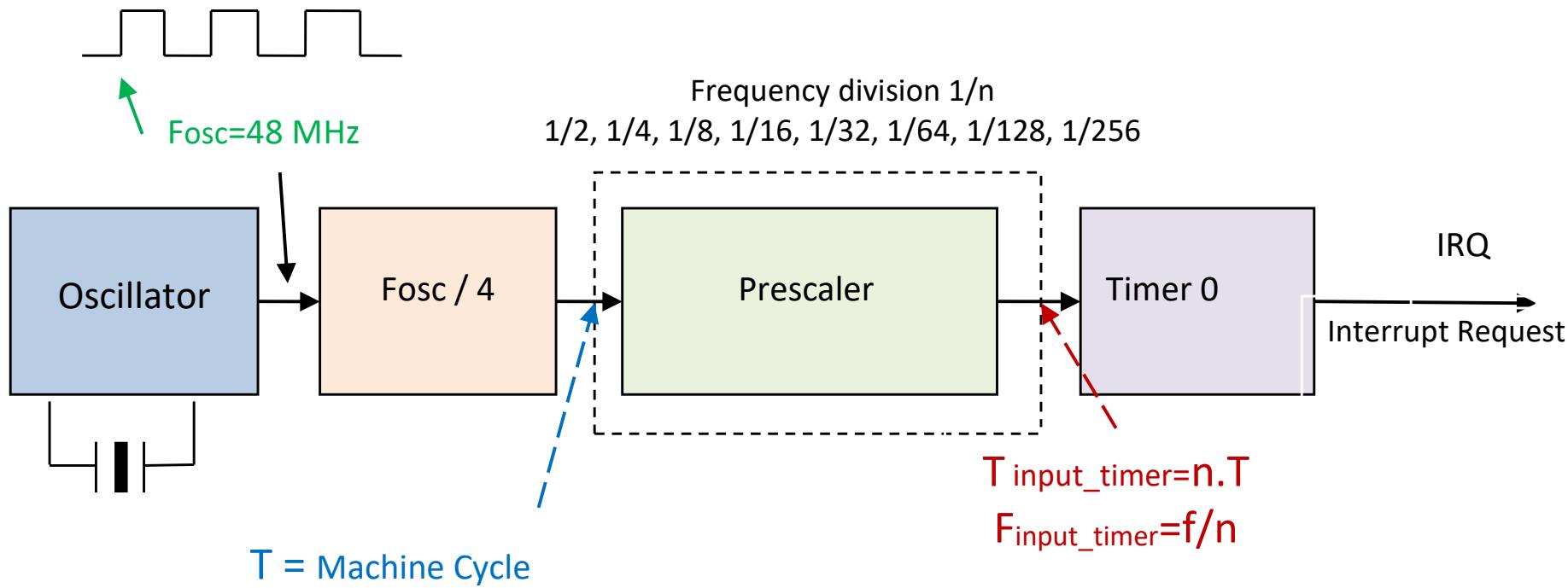
## Timer0

Timer0 is an 8 bit or 16 bit. If we want it to be used as an 8-bit timer, we should fill in the relevant parameter in the timer0 setup command.

```
setup_timer_0(T0_INTERNAL|T0_DIV_256|T0_8_BIT);
```

# Module\_2-8. Timers

## Timer



$T$  = Machine Cycle

$$T = \frac{1}{12\text{MHz}} = \frac{1}{12 \times 10^6 \text{Hz}} = 0,08333 \times 10^{-6} = 83,33 \times 10^{-9} = 83,33 \text{ ns}$$

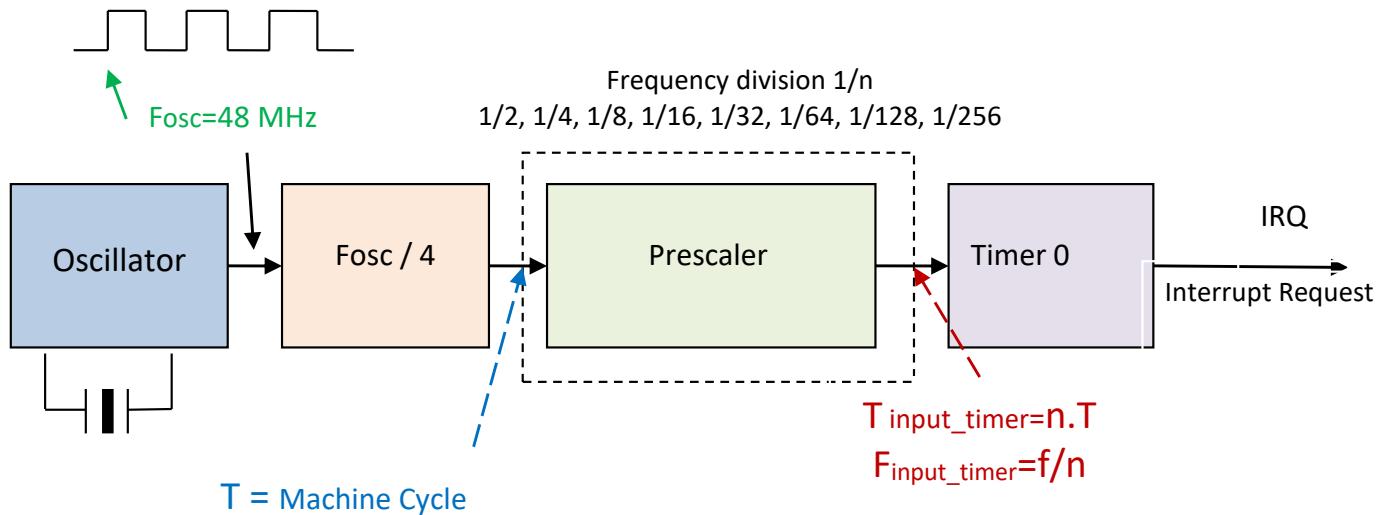
$\text{MC} = 83,33 \text{ ns}$        $\text{MC} \rightarrow$  Machine Cycle

$$f = \frac{1}{T} = 12 \text{ MHz} \text{ in the input of the prescaler}$$

Prescaler is a configurable clock-divider circuit. It can be used to divide the clock frequency input to the timer module. Prescaler's values: 1, 2, 4, 8, 16, 32, 64, 128, 256

# Module\_2-8. Timers

## Timer



**T** = Machine Cycle

$$T = \frac{1}{12\text{MHz}} = \frac{1}{12 \times 10^6 \text{Hz}} = 0,08333 \times 10^{-6} = 83,33 \times 10^{-9} = 83,33 \text{ ns}$$

MC = 83,33 ns      MC → Machine Cycle

$$f = \frac{1}{T} = 12 \text{ MHz in the input of the prescaler}$$

```
enable_interrupts(INT_TIMERO);
// Enable interrupt from timer0
```

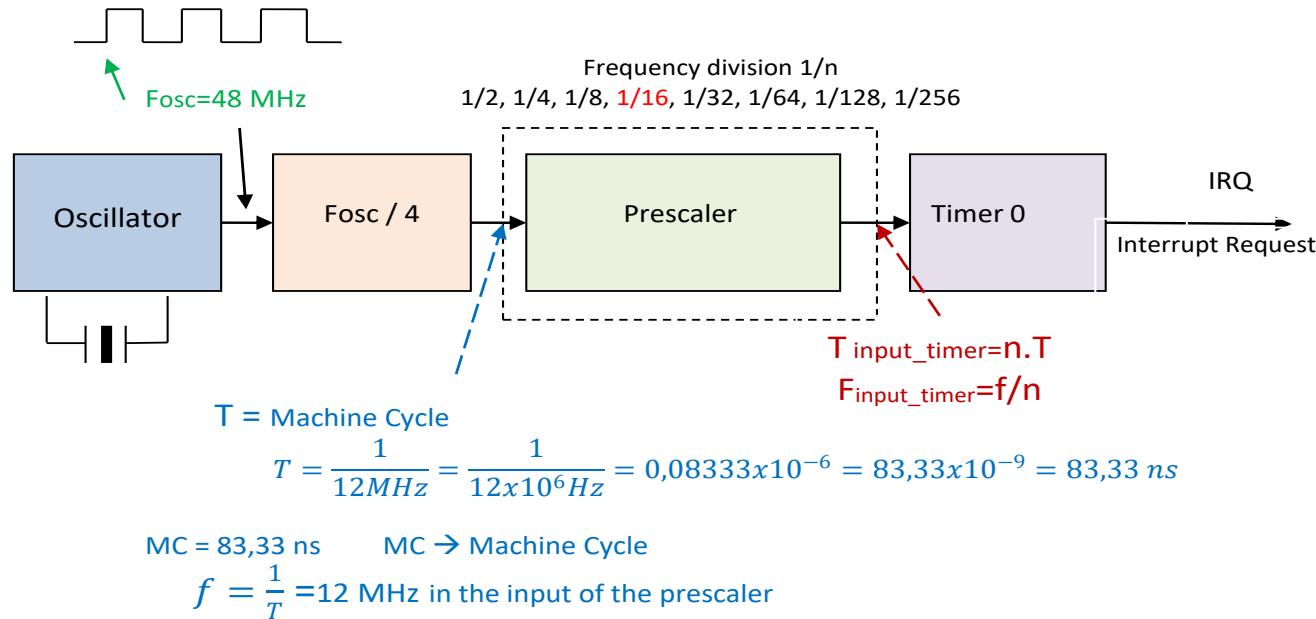
```
enable_interrupts(GLOBAL);
// enables (if set) all un-masked interrupts or disables (if cleared) all
interrupts.
```

```
setup_timer_0(T0_INTERNAL|T0_DIV_256);
```

## Module\_2-8. Timers

### Timer

Calculation so that interrupts occur at a specific time interval, for example every 50 ms



$$\begin{aligned} \text{Tinput-timer} &= 16 \times T \\ \text{Tinput-timer} &= 16 \times 83,33 \text{ ns} = 1333,28 \text{ ns} = 1,33328 \mu\text{s} \end{aligned}$$

In order to have interruptions every 50 ms, the initial value  $y$  set in the timer after each interruption should satisfy the equation:

$$(65536 - y) \times 1,33328 \mu\text{s} = 50000 \mu\text{s} \Leftrightarrow 65536 - y = \frac{50000}{1,33328} = 37501$$

$$y = 65536 - 37501 = 28035$$

This is the initial value that should be set to the Timer so that we have interrupts every 50ms

## *Module\_2-8. Timers*

### *Timer*

How do we set it up so that after every interruption the Timer0 does not take the initial value 0 but the value 28035?

```
#INT_TIMER0
// Directive that the next routine is its service routine for Timer0

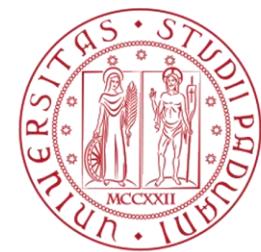
void timer0_int(void){
    set_timer0(28035)
    .....
    .....
    .....
}
```

## ENGINE Partnership

- Warsaw University of Technology (PL) - *coordinator*
- IHU - International Hellenic University (GR)
- EDUMOTIVA - European Lab for Educational Technology (GR)
- University of Padova (IT)
- University of Applied Sciences in Tarnow (PL)



INTERNATIONAL  
HELLENIC  
UNIVERSITY



UNIVERSITÀ  
DEGLI STUDI  
DI PADOVA



## CONTACT:



[www.engined.eu](http://www.engined.eu)



[angelika.tefelska@pw.edu.pl](mailto:angelika.tefelska@pw.edu.pl)



[@projectENGINE1](https://twitter.com/projectENGINE1)



[@EUprojectEngine](https://facebook.com/EUprojectEngine)



Erasmus+

This project has been funded with support from the European Commission. This publication reflects the views only of the author, and the Commission cannot be held responsible for any use which may be made of the information contained therein.